Fig. 1

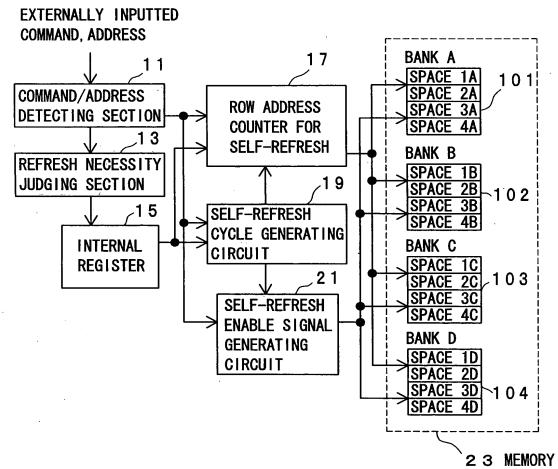
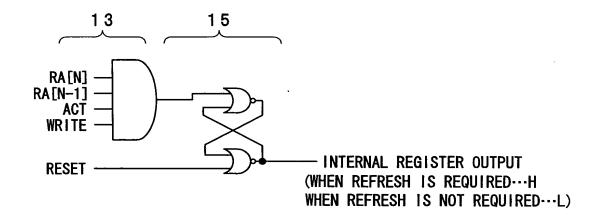


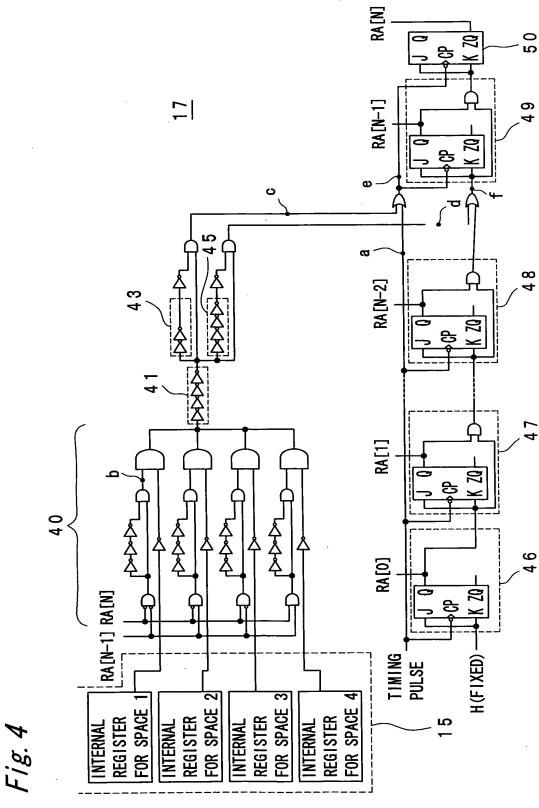
Fig. 2



6| REGISTER FOR SPACE 4 1-SPACE ENABLE SIGNAL 36 REFRESH SPACE NUMBER JUDGING SECTION FOR SPACE 3 INTERNAL Register TIMING 2-SPACE ENABLE SIGNAL မ ပ REGISTER FOR SPACE 2 3-SPACE ENABLE SIGNAL NTERNAL register For space 1 4-SPACE ENABLE SIGNAL INTERNAL ဗ

Fig. 3

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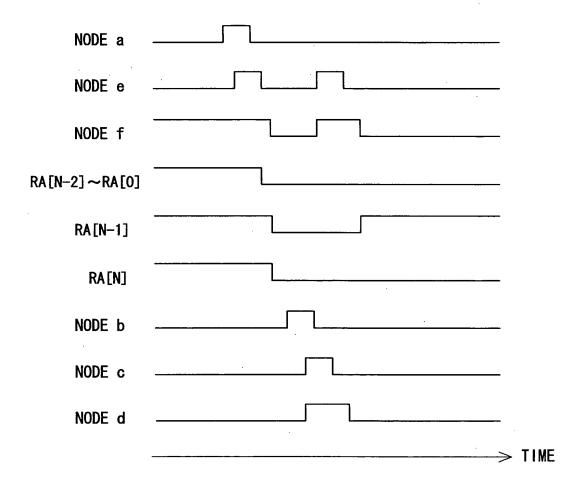


Fig. 6

